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O.I.P.E. PATENT DATE

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AF	PPLICATION NO. 09/853591	CONT/PRIOR F	CLASS 2.6 1	SUBCLASS	ART UNIT	EXAMINER	Rodraguer	-
APPLICANTS	Meng-Huan Chun-Hsia Shin Su Tao-Cheng	ng Lai						
TIME	RC contro	lled ESD	circuits	for mixec	I-voltage	interface		PTO-2040

ISSUING CLASSIFICATION							
ORIGIN	ORIGINAL CROSS REFERENCE(S)						
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INTERNATIONA	L CLASSIFICATION						
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DISCLAIMER	DRAWINGS			CLAIMS ALLOWED		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
The term of this patent		!		NOTICE OF ALLOWANCE MAILED		
subsequent to (date) has been disclaimed.	(Assistant I	Examiner)	(Date)			
The term of this patent shall not extend beyond the expiration date						
of U.S Patent. No.	ISSUE		UE FEE			
				Amount Due	Date Paid	
	(Primary Examiner)		(Date)			
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